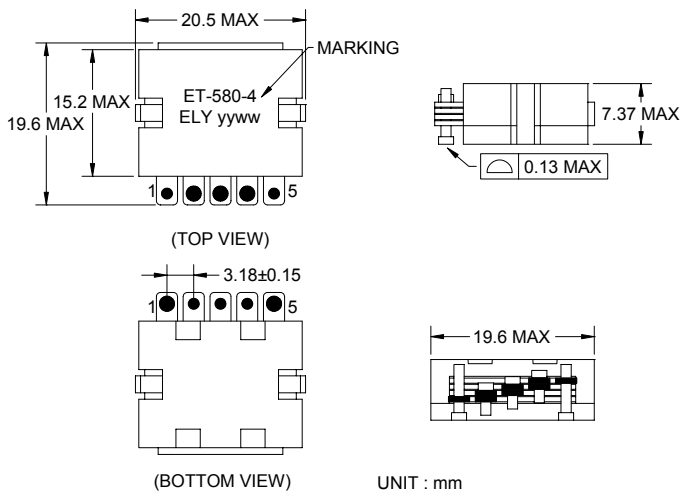


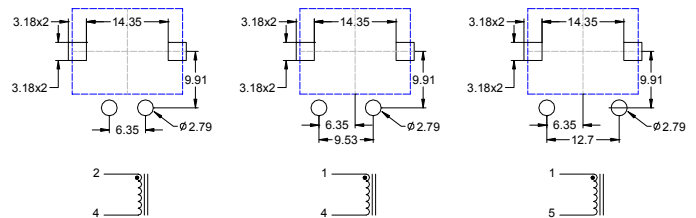
# ET-580 SERIES



- Designed for high current, high volt\*time applications
- Lowest DCR/uH, high efficiency
- Shielded construction



### PCB LAYOUT AND SCHEMATICS



Part No.	Inductance <sup>1</sup> ( $\mu$ H )		DCR ( m $\Omega$ )		I sat <sub>1</sub> ( A dc )
	@ 0 ADC	@ I sat	Typ.	max.	
<b>2-Turns ( Low-Loss ) Series</b>					
ET - 580 - 1 - R45	0.45	0.45	0.38	0.48	73
ET - 580 - 1 - R65	0.65	0.63	0.38	0.48	54
ET - 580 - 1 - R91	0.91	0.85	0.38	0.48	39
ET - 580 - 1 - 1R1	1.10	1.05	0.38	0.48	30
ET - 580 - 1 - 1R3	1.30	1.25	0.38	0.48	25
ET - 580 - 1 - 1R5	1.50	1.45	0.38	0.48	21
<b>2-Turns Series</b>					
ET - 580 - 2 - R45	0.45	0.45	0.78	0.98	52
ET - 580 - 2 - R65	0.65	0.63	0.78	0.98	52
ET - 580 - 2 - R91	0.91	0.85	0.78	0.98	39
ET - 580 - 2 - 1R1	1.10	1.05	0.78	0.98	30
ET - 580 - 2 - 1R3	1.30	1.25	0.78	0.98	25
ET - 580 - 2 - 1R5	1.50	1.45	0.78	0.98	21
<b>3-Turns Series</b>					
ET - 580 - 3 - 1R0	1.00	0.95	1.15	1.43	42
ET - 580 - 3 - 1R5	1.50	1.40	1.15	1.43	36
ET - 580 - 3 - 2R0	2.00	1.90	1.15	1.43	25
ET - 580 - 3 - 2R5	2.50	2.40	1.15	1.43	20
ET - 580 - 3 - 3R0	3.00	2.80	1.15	1.43	15
ET - 580 - 3 - 3R5	3.50	3.40	1.15	1.43	12
<b>4-Turns Series</b>					
ET - 580 - 4 - 1R6	1.60	1.60	1.44	1.80	37
ET - 580 - 4 - 2R42	2.42	2.40	1.44	1.80	30
ET - 580 - 4 - 3R6	3.60	3.30	1.44	1.80	17
ET - 580 - 4 - 4R4	4.40	4.00	1.44	1.80	14
ET - 580 - 4 - 5R34	5.34	4.90	1.44	1.80	11
ET - 580 - 4 - 6R2	6.20	5.80	1.44	1.80	9

1. Inductance tested at 100 KHz, 0.1 Vrms
2. Inductance tolerance  $\pm$  15 %
3. Inductance drop = 15 % typ. at rated Isat.
4. T = 45  $^{\circ}$ C rise typ.
5. Operating temperature range  $-40^{\circ}$ C to  $+130^{\circ}$ C
6. Electrical specifications at 25  $^{\circ}$ C